

entry (0x8606270, LLVM BB @0x8602a00):
%r2 = OR4 %r3, %r3
%cr0 = CMPWI %r2, 0
COND_BRANCH %cr0, 27, mbb<entry.no_exit_llvm_crit_edge,0x8606370>
Successors according to CFG: 0x8606370 0x86062f0

entry.no_exit_llvm_crit_edge (0x8606370, LLVM BB @0x8605b30):
%r3 = LI 0
%r4 = OR4 %r3, %r3
Successors according to CFG: 0x86063f0

entry.loopexit_llvm_crit_edge (0x86062f0, LLVM BB @0x8605d60):
%r2 = LI 0
B mbb<loopexit,0x8606670>
Successors according to CFG: 0x8606670

no_exit (0x86063f0, LLVM BB @0x8602b10):
%r3 = PHI %r4, mbb<entry.no_exit_llvm_crit_edge,0x8606370>, %r3, mbb<no_exit.no_exit_llvm_crit_edge,0x86065f0>
%r4 = PHI %r3, mbb<entry.no_exit_llvm_crit_edge,0x8606370>, %r4, mbb<no_exit.no_exit_llvm_crit_edge,0x86065f0>
%r5 = LIS 21845
%r6 = SUBF %r3, %r2
%r5 = ORI %r5, 21846
%r5 = MULHW %r6, %r5
%r7 = ADDI %r6, 4294967295
%r8 = LI 4294967295
%r9 = RLWINM %r5, 1, 31, 31
%r5 = ADD4 %r5, %r9
%r5 = MULLI %r5, 3
%r9 = LI 1
%cr0 = CMPWI %r7, 0
%r5 = SUBF %r5, %r6
%cr1 = CMPWI %r5, 0
BEQ %cr1, mbb<no_exit,0x8609580>
Successors according to CFG: 0x8607050 0x8609580

no_exit (0x8607050, LLVM BB @0x8602b10):
Successors according to CFG: 0x8609580

no_exit (0x8609580, LLVM BB @0x8602b10):
%r5 = PHI %r9, mbb<no_exit,0x8607050>, %r8, mbb<no_exit,0x86063f0>
%r3 = ADDI %r3, 1
%r4 = ADD4 %r5, %r4
COND_BRANCH %cr0, 27, mbb<no_exit.no_exit_llvm_crit_edge,0x86065f0>
Successors according to CFG: 0x86065f0 0x8606550

no_exit.no_exit_llvm_crit_edge (0x86065f0, LLVM BB @0x8605e50):
B mbb<no_exit,0x86063f0>
Successors according to CFG: 0x86063f0

no_exit.loopexit_llvm_crit_edge (0x8606550, LLVM BB @0x8605fe0):
B mbb<loopexit,0x8606670>
Successors according to CFG: 0x8606670

loopexit (0x8606670, LLVM BB @0x8602b70):
%r2 = PHI %r2, mbb<entry.loopexit_llvm_crit_edge,0x86062f0>, %r4, mbb<no_exit.loopexit_llvm_crit_edge,0x8606550>
%r3 = LIS <ga:.str_1>
%r3 = LA %r3, <ga:.str_1>
ADJCALLSTACKDOWN 56
%r4 = OR4 %r2, %r2
BL <ga:printf>, %r3, %r4
ADJCALLSTACKUP 56
%r2 = IMPLICIT_DEF_GPR
%r3 = OR4 %r2, %r2
BLR