

entry (0x9105f40, LLVM BB @0x9102a20):  
%r2 = OR4 %r3, %r3  
%cr0 = CMPWI %r2, 0  
COND\_BRANCH %cr0, 27, mbb<entry.no\_exit\_crit\_edge,0x9106490>  
Successors according to CFG: 0x9106490 0x9106450

entry.no\_exit\_crit\_edge (0x9106490, LLVM BB @0x9106070):  
%r3 = LI 0  
Successors according to CFG: 0x91037b0

no\_exit (0x91037b0, LLVM BB @0x9102b30):  
%r2 = PHI %r2, mbb<entry.no\_exit\_crit\_edge,0x9106490>, %r5, mbb<no\_exit,0x91092e0>  
%r3 = PHI %r3, mbb<entry.no\_exit\_crit\_edge,0x9106490>, %r2, mbb<no\_exit,0x91092e0>  
%r4 = LIS 21845  
%r4 = ORI %r4, 21846  
%r4 = MULHW %r2, %r4  
%r5 = ADDI %r2, 4294967295  
%r6 = LI 4294967295  
%r7 = RLWINM %r4, 1, 31, 31  
%r4 = ADD4 %r4, %r7  
%r4 = MULLI %r4, 3  
%r7 = LI 1  
%r2 = SUBF %r4, %r2  
%cr0 = CMPWI %r2, 0  
BEQ %cr0, mbb<no\_exit,0x91092e0>  
Successors according to CFG: 0x9107260 0x91092e0

entry.loopexit\_crit\_edge (0x9106450, LLVM BB @0x9103210):  
%r2 = LI 0  
B mbb<loopexit,0x9102cb0>  
Successors according to CFG: 0x9102cb0

no\_exit (0x9107260, LLVM BB @0x9102b30):  
Successors according to CFG: 0x91092e0

no\_exit (0x91092e0, LLVM BB @0x9102b30):  
%r2 = PHI %r7, mbb<no\_exit,0x9107260>, %r6, mbb<no\_exit,0x91037b0>  
%cr0 = CMPWI %r5, 0  
%r2 = ADD4 %r2, %r3  
COND\_BRANCH %cr0, 27, mbb<no\_exit,0x91037b0>  
Successors according to CFG: 0x91037b0 0x9102cb0

loopexit (0x9102cb0, LLVM BB @0x9102b90):  
%r2 = PHI %r2, mbb<entry.loopexit\_crit\_edge,0x9106450>, %r2, mbb<no\_exit,0x91092e0>  
%r3 = LIS <ga:.str\_1>  
%r3 = LA %r3, <ga:.str\_1>  
ADJCALLSTACKDOWN 56  
%r4 = OR4 %r2, %r2  
BL <ga:printf>, %r3, %r4  
ADJCALLSTACKUP 56  
%r2 = IMPLICIT\_DEF\_GPR  
%r3 = OR4 %r2, %r2  
BLR

CFG for 'main' function